

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Sokolov et al.

Attorney Docket No.: SUN1P810/P5510

Application No.: 09/703,356

Examiner: Opie, G.

Filed: October 31, 2000

Group: 2194

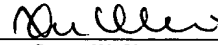
Title: METHODS AND APPARATUS FOR
NUMERIC CONSTANT VALUE INLINING IN
VIRTUAL MACHINES

Confirmation No.: 3230

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on May 23, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signed: _____


Susan W. Xu

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AFTER FINAL ACTION OR NOTICE OF ALLOWANCE**

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Dear Sir:

The references listed in the attached PTO Form 1449 may be material to examination of the above-identified patent application. Applicants identify these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these citations of official record in this application. Please note that references listed under Foreign Patent or Published Foreign Patent Application and Other Documents are enclosed herewith.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

05/25/2005 CCHAU1 00000047 09703356

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This Information Disclosure Statement is being filed after the mailing date of a notice of allowance under §1.311, but before payment of the issue fee.

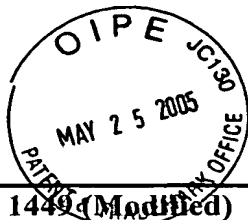
Accompanying this Information Disclosure Statement is the fee set forth in 37 CFR 1.17(p).

If fees are due, enclosed is our Check No. 26318 for \$180.00 in payment of the Information Disclosure Statement Fee. If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SUN1P810).

Respectfully submitted,
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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. SUN1P810/P5510	Application No.: 09/703,356
	Applicant: Sokolov et al. Filing Date October 31, 2000	Group 2126

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1	2002/0046298A1	04/18/02	Bak et al.			06/30/97
	A2	2002/0170041A1	11/14/02	Shann			12/20/01
	A3	2002/0170043A1	11/14/02	Bagley et al.			02/23/01
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	A7	5,815,718	09/29/98	Tock			05/30/96
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Examiner				Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	October 31, 2000	2126

U.S. Patent Documents

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	A33	6,446,254 B1	09/03/02	Chapman et al.			08/13/99
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	A39	6,851,108 B1	02/01/05	Syme et al.			11/16/00
	A40	6,851,111 B2	02/01/05	McGuire et al.			12/15/00

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1	WO01/22213A2	03/29/01	PCT				
	B2	WO02/077807A1	10/03/02	PCT				
	B3	WO02/41145 A2	05/23/02	PCT				

Other Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	C1	Lambright H D, "Java Bytecode Optimizations" Proceedings of IEEE Compcon '97. San Jose, Feb. 23-26, 1997, pages 206-210, XP-000751784					
	C2	Accomazzo E et al., "Integrating intermediate Code Optimization with Retargetable Code Generation" Microprocessing and Microprogramming, Elsevier Science Publishers, BV. Amsterdam, NL, vol. 30, No. 1/5, August 1, 1990, pages 475-481, XP000141684					
	C3	Tim Ritchey, "Java!" 1995, New Riders Publishing, pgs, 326-343					
Examiner				Date Considered			

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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C4	Jon Meyer & Troy Downing, "Java Virtual Machine," 1997, O'Reilly & Associates, Inc. pgs. 46, 56-57, 74-75, 389 and 391
	C5	Microsoft Press Computer Dictionary Third Edition, pgs. 294-295
	C6	Piumarta et al., "Optimizing Direct Threaded Code by Selective Inlining," Association for Computing Machinery, Vol. 33, No. 5, pp. 291-300, May 1, 1998.XP-000766278
	C7	Tommesani, "Programming models"
	C8	Goldberg, "A specification of java loading and bytecode verification," ACM, pp 49-58, 1998
	C9	Chang et al. "Type based verification of assembly language for compiler degugging," ACM TLDI, pp 91-102, Jan. 2005
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	C11	Krintz et al., "Reducing transfer delay using java class file splitting and prefetching," ACM OOPSLA, pp276-291 11/1999
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	C14	Chilimbi et al., "Cache-Conscious Structure Definition", P. 13-24, 1999 ACM
	C15	Nicol et al., "Empirical Study of parallel Trace-Driven LRU Cache Simulators", p. 166-169, 1995 IEEE
Examiner		Date Considered

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